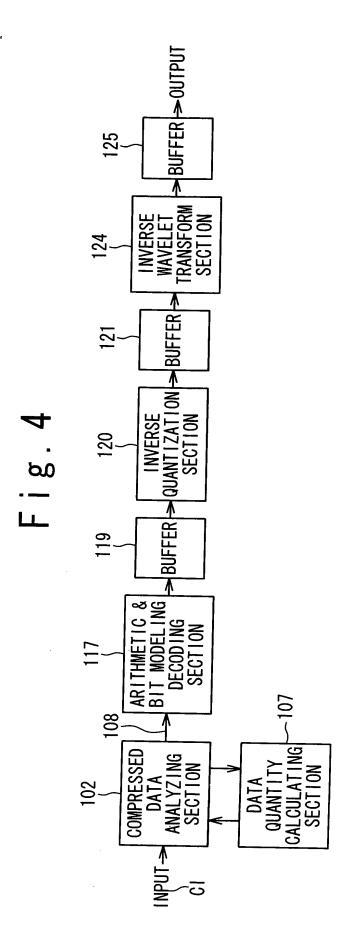


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INVERSE WAVELET TRANSFORM SECTION 124 SECT I ON 120 ARITHMETIC DECODING SECTION BIT MODELING DECODING SECTION 8 DATA 108 COMPRESSED DATA ANALYZING SECTION 102 ADDRESS GENERATING CIRCUIT 106 105~ CODE BUFFER 103 4 COMPRESSED DATA-> CI

Fig. 2



F. 5

